

<p style="text-align: right;">TYPE FORM 1449*</p> <p style="text-align: center;">INFORMATION DISCLOSURE STATEMENT</p> <p style="text-align: center;">IN AN APPLICATION</p> <p style="text-align: center;">(Use several sheets if necessary)</p> <p style="text-align: left; margin-left: 10px;">DEC 29 2003</p> <p style="text-align: left; margin-left: 10px;">PATENT & TRADEMARK OFFICE</p>	Docket Number:	Application Number:
	10873.1316US01	
	10/690,957	
	Applicant: OGAWA et al.	
Filing Date: October 21, 2003	Group Art Unit: Unknown <i>2825</i>	

U.S. PATENT DOCUMENTS

FOREIGN PATENT DOCUMENTS

	DOCUMENT NO.	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
						YES	NO
B7	2001-229217	08.2001	Japan			See IDS	

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

		Gajski, D. and N. Dutt et al., HIGH-LEVEL SYNTHESIS, Introduction to Chip and System Design, Kluwer Academic Publishers, 1992
		Kaneko, M., et al., "Assignment Based Approach to High Level Synthesis for Net Relevant Design Criteria", in Technical Report of IEICE., VID 98-147, ICD98-293(1999-03), The Institute of Electronics, Information and Communication Engineers., pp 49-56

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PATENT TRADEMARK OFFICE		
		DATE CONSIDERED <i>11/12/02</i>
EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form for next communication to the Applicant.		

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